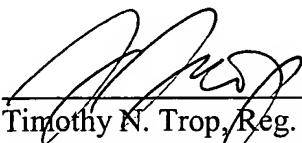# REMARKS

Claim 1 *inter alia* calls for copying an address of a first descriptor to a two address buffer. In response thereto, the office action suggests that, for some reason which is unexplained, the provision of two regions in the cited Poulsen patent constitutes a two address buffer.

This conclusion is drawn without any explanation. Moreover, it is drawn without any effort to show that Poulsen even talks about any kind of buffer, much less a buffer with two addresses.

To the contrary, the recitation of two regions is, in fact, a reference to two regions of execution within a given computer program. It has nothing whatsoever to do with a buffer or even a buffer with two addresses. See Poulsen at column 6, lines 36-41. Therefore, reconsideration is requested since there is no basis for interpreting the parallel regions of execution of a computer program as a two address buffer.

Respectfully submitted,

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